

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE
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ATTY. DOCKET NO.

108339-09059

SERIAL NO.

09/714,273

LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

APPLICANT

Govind MALALUR

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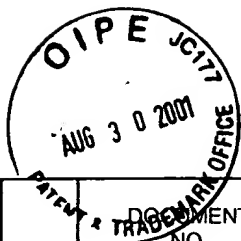
November 17, 2000

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	AB	5,644,784	Jul. 1, 1997	Peek			Mar. 3, 1995
	AD	5,828,653	Oct. 27, 1998	Goss			Apr. 26, 1996
	AE	5,423,015	Jun. 6, 1995	Chung			May 21, 1991
	AF	5,696,899	Dec. 9, 1997	Kalwitz			Nov. 18, 1992
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	AH	5,748,631	May 5, 1998	Bergantino et al.			May 9, 1996
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	AN	5,892,922	Apr. 6, 1999	Lorenz			Feb. 28, 1997
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	BF	5,898,687	Apr. 27, 1999	Harriman et al.			Jul. 24, 1996
	BG	5,940,596	Aug. 17, 1999	Rajan et al.			Aug. 4, 1997
	BH	5,802,052	Sep. 1, 1998	Venkataraman			Jun. 26, 1996
	BI	5,842,038	Nov. 24, 1998	Williams et al.			Oct. 10, 1996
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	BP	EP0465090A1	Jan. 8, 1992	EPO					
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	CJ	"Local Area Network Switch Frame Lookup Technique for Increased Speed and Flexibility," 700 IBM Technical Disclosure Bulletin 38(1995) July, No. 7, Armonk, NY, US, pages 221-222
	CK	"Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches," Yu-Sheng Lin et al., Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996, pages 688-695
	CL	"A 622-Mb/s 8 x 8 ATM Switch Chip Set with Shared Multibuffer Architecture," Harufusa Kondoh et al., 8107 IEEE Journal of Solid-State Circuits 28(1993) July, No. 7, New York, US, pages 808-814
	CM	"Catalyst 8500 CSR Architecture," White Paper XP-002151999, Cisco Systems Inc. 1998, pages 1-19
	CN	"Computer Networks," A.S. Tanenbaum, PRENTICE-HALL INT., USA, XP-002147300(1998), Sec. 5.2-Sec. 5.3, pages 309-320

EXAMINER	Sana Al-hashemi	DATE CONSIDERED	7/8/03
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			

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